

TIME TABLE

Semester: ODD Year: 2026-2027

Class: M. TECH-I CS & VL

Class: M. TECH-I CS & VL									
Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30 - 9.20	9.30 -10.20	10.30 - 11.20	11.30 - 12.20	12.20 - 2.00	2.00 -2.50	3.00 -3.50	4.00 -4.50	5.00 - 5.50
Monday	A1 (T&V)	B2 (SIT)	C3(RFCS)	D4(ITC) (MOS)		G5 (PA)			
Tuesday	B1 (DVD)	C2 (AOCS) (T&V)	D3 (ADSP) (MOS)	E4(ITC) (ES)				P12 (RFCS)	
							I6 (PA)	J7 (RFCS)	
Wednesday	C1 (ES)	D2(AOCS)	E3(ADSP)	A4 (SIT)					
						I5 (PA)	J6 (DVD)		
Thursday	D1(RFCS) (MOS)	E2(ADSP) (SIT)	A3 (DVD)					P16 (AOCS) & (MOS)	
Friday		A2(ITC) (T&V)	B3 (ES)	C4(AOCS)				P18(ADSP)	
							P19 (DVD)		P20(ES)

Sr. No.	SLOT	COURSE NO	TITLE	FACULTY	CREDIT	CLASSROOM
(1)	C3,D1,J7	ECCS101	RF Circuits & Systems	KI,KI,KI	3-0-0	EC303N
(2)	C2,D2	ECCS103	Advance Optical Communication Systems	PNP,PNP	3-0-0	EC303N
(3)	C4	ECCS103	MOS Device and Technology	PNP	3-0-0	EC201N
(4)	D3	ECCS105	Advance DSP	SD	3-0-0	EC303N
(5)	E2,E3	ECCS105	Advance DSP	SD,SD	3-0-0	EC406N
(6)	D4,E4,A2	ECCS111	Information Theory & Coding	JNS,JNS,JNS	3-0-0	EC303N
(7)	B1,J6,A3	ECVL101	Digital VLSI Design	PD,PD,PD	3-0-0	EC222N
(8)	D3,D1	ECVL103	MOS Devices and Technology	PS,PS,PS	3-0-0	EC222N
(9)	D4	ECVL103	MOS Devices and Technology	PS,PS,PS	3-0-0	EC105N
(10)	C1,B3	ECVL105	Embedded Systems	PJE,PJE,PJE	3-0-0	EC222N
(11)	E4	ECVL105	Embedded Systems	PJE,PJE,PJE	3-0-0	EC105N
(12)	A1,C2,A2	ECVL117	Testing and Verification of VLSI Circuits	DJ,DJ,DJ	3-0-0	EC222N
(13)	B2,E2	ECVL111	Semiconductor IC Technology	VG,VG,VG	3-0-0	EC222N
(14)	A4	ECVL111	Semiconductor IC Technology	VG,VG,VG	3-0-0	EC202N
(15)	G5,I6,I5	ECVL115	Processor Architecture	PJE,PJE,PJE	3-0-0	EC222N
(16)	P12	ECCS101	RF Circuits & Systems	KI,L6	0-0-6	EC403N
(17)	P16	ECCS103	Advance Optical Communication Systems	PNP,L7	0-0-6	EC403N
(18)	P18	ECCS105	Advance DSP	SD,P2	0-0-6	EC303N
(19)	P19	ECVL101	Digital VLSI Design	PD,L4	0-0-6	EC306N
(20)	P16	ECVL103	MOS Devices and Technology	PS,L1	0-0-6	EC306N
(21)	P20	ECVL105	Embedded Systems	PJE,L2	0-0-6	EC307N

